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Batch A

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PREFACE

The CoreConferences 2019 held on 20th – 21st March, 2019, in collaboration with Association of Scientists, Developers and Faculties (ASDF), an International body, at Taipei, Taiwan.

CoreConferences 2019 provides a chance for Academic and Industry professionals to discuss the recent progress in the area of Multiple. The outcome of the conference will trigger for the further related research and future technological improvement. This conference highlights the novel concepts and improvements related to the research and technology.

The technical committee consists of experts in the various course subfields helped to scrutinize the technical papers in various fields, support to maintain the quality level of the proceedings of conference which consist of the information of various advancements in the field of research and development globally and would act as a primary resource of researchers to gain knowledge in their relevant fields.

The constant support and encouragement from Dr. S. Prithiv Rajan, ASDF Global President, Dr. Julie Rue Bishop, ASDF International President and Dr. K. Kokula Krishna Hari, ASDF International General Secretary helped a lot to conduct the conference and to publish the proceedings within a short span. I would like to express my deep appreciation and heartfelt thanks to the ASDF team members. Without them, the proceedings could not have been completed in a successful manner. I would like to express my sincere thanks to our management, student friends and colleagues for their involvement, interest, enthusiasm to bring this proceeding of the conference in a successful way.

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RRI Buffer Based Energy and Computation Efficient Cache Replacement Algorithm

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Abstract: *Energy consumption is an important factor of com-mutational power these days. Large scale energy consumption results in bad system performance and high cost. To access frequently used data, we place it in Cache. Cache provides us opportunity to access that data in a small time. Cache memory helps in retrieving data in minimum time improving the system performance and reducing power consumption. Due to limited size of Cache, replacement algorithms used to make space for new data. There are many existing cache replacement algorithms for example LRU, LFU, MRU, FIFO etc. Existing algorithms consume a lot of energy while replacing cold blocks of data. Replacement algorithms are usually designed to reduce miss rate and increase hit rate. These algorithms replace cold blocks (not going to use in future) and due to large number of cold blocks, they consume lot of energy. This paper proposes an energy and computation efficient cache replacement algorithm that put only hot blocks in action instead of removing cold blocks. This paper also discusses different replacement algorithms proposed in different papers and compare these algorithms on basis of different parameters mainly energy consumption. In our experiments we have found LRU and FIFO as best replacement algorithms for Increased hit rates and Energy efficiency respectively.*

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Key Performance Index of Increasing Air Quality with Construction Schedule Control

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Abstract: *Recently, air quality in residential spaces has been major concern. In particular, the indoor air quality of residential facility before occupancy, which is related to the interior material, is a serious problem. existing research has mainly focused on pollution control after construction, but this research has derived I key performance index I about increasing air quality and priority of management with a controlling schedule. That is the objectives of research. The results show the relative priority of the four major items in wall-based apartment buildings and in column-based apartment buildings. An analysis of the parties responsible for improvement based on the IAQ results shows more efforts to improve IAQ are needed in material factories and engineering / design companies.*

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